



IPW

PATENT
Customer No. 22,852
Attorney Docket No. 03180.0338

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
)
Masao Iwase et al.) Group Art Unit: 2811
)
Serial No.: 10/687,705) Examiner: Sara W. Crane
)
Filed: October 20, 2003)
) Confirmation No.: 2832
For: SEMICONDUCTOR WAFER AND)
A METHOD FOR)
MANUFACTURING A)
SEMICONDUCTOR WAFER)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

RESPONSE TO RESTRICTION REQUIREMENT

In a restriction requirement dated February 9, 2005, the Examiner required restriction under 35 U.S.C. § 121 between Group I, claims 1-30, characterized by the Examiner as drawn to a semiconductor wafer; and Group II, claims 31-36, characterized by the Examiner as drawn to a method of making a semiconductor wafer. Applicants provisionally elect to prosecute Group I, claims 1-30 without traverse.

Please grant any extensions of time required to enter this response and charge any additional required fees to our deposit account 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.

Dated: March 8, 2005

By: 

Richard V. Burguijan
Reg. No. 31,744